



DOCKET NO. 01-035



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Applicant: Theodore Vaida, *et al.*

Serial No.: 09/851,181

Filed: May 8, 2001

Title: APPLICATION SPECIFIC INTEGRATED CIRCUIT HAVING A
PROGRAMMABLE LOGIC CORE AND A METHOD OF OPERATION
THEREOF

Grp./A.U.: 2661

Examiner: Phan, Tri. H.

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313, on	
4-8-2005	(Date)
Elizabeth Schumacher	
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Elizabeth Schumacher	
(Signature of the person signing the certificate)	

Sir:

DRAWING AMENDMENT UNDER 37 C.F.R. § 1.121

The attached sheets of drawings are formal drawings to replace the informal drawings initially filed with the above-identified application.

Attachment: Replacement Sheets: 1-6.